



FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

023925-00002

SERIAL NO.

09/805,903

LIST OF REFERENCES CITED BY APPLICANT

(Use several sheets if necessary)

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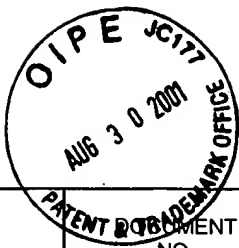
March 15, 2001

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EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
<i>[initials]</i>	AA	5,555,398	Sep. 10, 1996	Raman			Apr. 15, 1994
<i>[initials]</i>	AB	5,644,784	Jul. 1, 1997	Peek			Mar. 3, 1995
<i>[initials]</i>	AD	5,828,653	Oct. 27, 1998	Goss			Apr. 26, 1996
<i>[initials]</i>	AE	5,423,015	Jun. 6, 1995	Chung			May 21, 1991
<i>[initials]</i>	AF	5,696,899	Dec. 9, 1997	Kalwitz			Nov. 18, 1992
<i>[initials]</i>	AG	6,061,351	May 9, 2000	Erimli et al.			Dec. 18, 1997
<i>[initials]</i>	AH	5,748,631	May 5, 1998	Bergantino et al.			May 9, 1996
<i>[initials]</i>	AI	6,119,196	Sep. 12, 2000	Muller et al.			Jun. 30, 1997
<i>[initials]</i>	AJ	6,175,902 B1	Jan. 16, 2001	Runalduet et al.			Dec. 18, 1997
<i>[initials]</i>	AK	5,473,607	Dec. 5, 1995	Hausman et al.			Aug. 9, 1993
<i>[initials]</i>	AL	5,787,084	Jul. 28, 1998	Hoang et al.			Jun. 5, 1996
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<i>[initials]</i>	AN	5,892,922	Apr. 6, 1999	Lorenz			Feb. 28, 1997
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<i>[initials]</i>	AP	6,011,795	Jan. 4, 2000	Varghese et al.			Mar. 20, 1997
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<i>[initials]</i>	AT	5,831,980	Nov. 3, 1998	Varma et al.			Sep. 13, 1996
<i>[initials]</i>	AU	5,781,549	Jul. 14, 1998	Dai			Feb. 23, 1996
<i>[initials]</i>	AV	6,041,053	Mar. 21, 2000	Douceur et al.			Sep. 18, 1997
<i>[initials]</i>	AW	5,459,717	Oct. 17, 1995	Mullan et al.			Mar. 25, 1994
<i>[initials]</i>	AX	5,909,686	Jun. 1, 1999	Muller et al.			Jun. 30, 1997
<i>[initials]</i>	AY	5,887,187	Mar. 23, 1999	Rostoker et al.			Apr. 14, 1997
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<i>[initials]</i>	BA	5,414,704	May 9, 1995	Spinney			Apr. 5, 1994
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<i>JD</i>	BF	5,898,687	Apr. 27, 1999	Harriman et al.			Jul. 24, 1996
<i>JD</i>	BG	5,940,596	Aug. 17, 1999	Rajan et al.			Aug. 4, 1997
<i>JD</i>	BH	5,802,052	Sep. 1, 1998	Venkataraman			Jun. 26, 1996
<i>JD</i>	BI	5,842,038	Nov. 24, 1998	Williams et al.			Oct. 10, 1996
<i>JD</i>	BJ	5,742,613	Apr. 21, 1998	MacDonald			Apr. 27, 1993
<i>JD</i>	BK	5,579,301	Nov. 26, 1996	Ganson et al.			Feb. 28, 1994
<i>JD</i>	BL	5,278,789	Jan. 11, 1994	Inoue et al.			Dec. 6, 1991
<i>JD</i>	BM	5,652,579	Jul. 29, 1997	Yamada et al.			Aug. 15, 1996
<i>JD</i>	BN	5,499,295	Mar. 12, 1996	Cooper			Aug. 31, 1993

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<i>JD</i>	BO	WO9900936A1	Jun. 12, 2001	WIPO					
<i>JD</i>	BP	EP0465090A1	Jan. 8, 1992	EPO					
<i>JD</i>	BQ	EP0859492A2	Aug. 19, 1998	EPO					
<i>JD</i>	BR	EP0312917A2	Apr. 26, 1989	EPO					
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<i>JD</i>	BT	4-189023	Jul. 7, 1992	Japan					Abs
<i>JD</i>	BU	WO 98/09473	Mar. 5, 1998	WIPO					
<i>JD</i>	BV	EP0752796A2	Jan. 8, 1997	EPO					
<i>JD</i>	BW	EP0862349A2	Sep. 2, 1998	EPO					Abs
<i>JD</i>	BX	FR 2 725 573 - A1	Apr. 12, 1996	France					Abs
<i>JD</i>	BY	EP0854606A2	Jul. 22, 1998	EPO					
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<i>JD</i>	CC	WO 99/00939	Jan. 7, 1999	WIPO					
<i>JD</i>	CD	WO 99/00938	Jan. 7, 1999	WIPO					
<i>JD</i>	CE	WO 99/00944	Jan. 7, 1999	WIPO					
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<i>JD</i>	CG	EP0849917A2	Jun. 24, 1998	EPO					
<i>JD</i>	CH	EP0907300A2	Apr. 7, 1999	EPO					

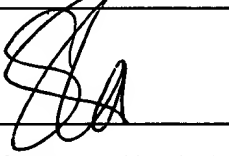

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	CJ	"Local Area Network Switch Frame Lookup Technique for Increased Speed and Flexibility," 700 IBM Technical Disclosure Bulletin 38(1995) July, No. 7, Armonk, NY, US, pages 221-222
	CK	"Queue Management for Shared Buffer and Shared Multi-buffer ATM Switches," Yu-Sheng Lin et al., Department of Electronics Engineering & Institute of Electronics, National Chiao Tung University, Hsinchu, Taiwan, R.O.C., March 24, 1996, pages 688-695
	CL	"A 622-Mb/s 8 x 8 ATM Switch Chip Set with Shared Multibuffer Architecture," Harufusa Kondoh et al., 8107 IEEE Journal of Solid-State Circuits 28(1993) July, No. 7, New York, US, pages 808-814
	CM	"Catalyst 8500 CSR Architecture," White Paper XP-002151999, Cisco Systems Inc. 1998, pages 1-19
	CN	"Computer Networks," A.S. Tanenbaum, PRENTICE-HALL INT., USA, XP-002147300(1998), Sec. 5.2-Sec. 5.3, pages 309-320

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Sheng Feng CHIEN

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	AI								
	AJ								
	AK								

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	AM	
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